Extreme Networks Field Notice: 0016
MSM64i – Master/Slave Relationship at Boot Up
April 20, 2001

Products Affected:
BlackDiamond MSM64i – PN: 50015

Problem Description:
When installing one or both MSM modules, the MSM in slot B always comes up as the Master MSM. Typically upon initialization, MSM-A is the Master. In this scenario, there are no errors or indication that the MSM in slot A has any problems.

Problem Symptoms:
MSM64i boards that are built with an AMI-based, CPU support ASIC boot up quicker than boards that are built with a Xilinx FPGA-based CPU support system. A BlackDiamond that has a combination of these two different types of boards may exhibit this condition because the AMI-based MSM will initialize first and claim to be master. This will only occur if a FPGA board is in slot A and an AMI board is in slot B.

The difference in the two devices is noted by a change in part number on the daughter card of the MSM module. Refer to Figure 1 and 2 attached to verify the condition.

Workaround:
The faster MSM (AMI board) can be swapped into slot A. This should be done to verify and confirm that there is no operational problem with either MSM or the BlackDiamond chassis.

Solution:
There are no functional consequences of MSM B working as master. If a fail-over is required, MSM A will take over as the new master. In the case that MSM B boots as master, use the “show log”, “show switch”, and “show diag”commands from the CLI to verify that there are no errors with the MSM in slot A.

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Figure 1 shows the MSM with the front face plate at the bottom of the picture. The daughter card is the top card with the bar coded white label in the upper left hand corner of the card as show in this picture.

Figure 1

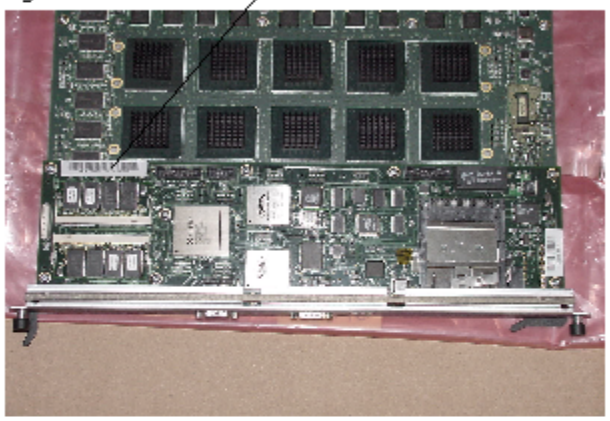
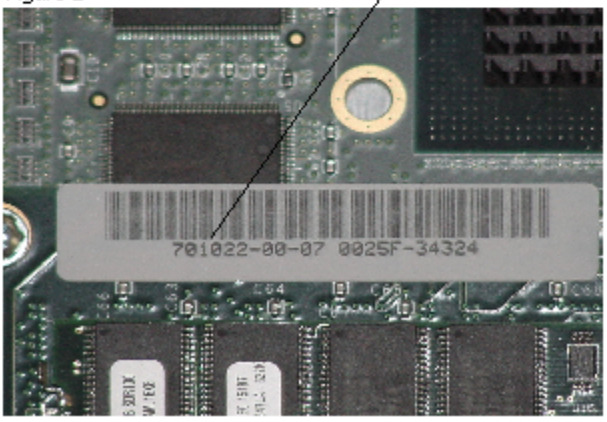


Figure 2 is up close picture of the bar coded label. The first 6 digits on the bar code label indicate the part number. In this example, 701022 is the part number of the daughter card. The part number of each device is as follows;

AMI ASIC (fast) - PN: 701025
Xilinx FPGA (slow) - PN: 701022

Figure 2



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For more information or questions regarding this Field Notice, please contact Extreme Networks Technical Support.

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